L Number	Hits	Search Text	DB	Time stamp
•	1	(arbitration with queue\$1 with (shar\$3 adj memory)) same	USPAT;	2004/03/01 08:58
]		cache	US-PGPUB;	
			EPO; JPO;	
[			DERWENT;	
		P	IBM_TDB	0004/00/04 00 50
-	0	mem. adj int.	USPAT;	2004/03/01 08:58
			US-PGPUB;	
			EPO; JPO; DERWENT;	
			IBM_TDB	
	163	xio	USPAT;	2004/03/01 08:58
	,,,,		US-PGPUB;	200 1100/01 00:00
		<u>.</u>	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	13	xio and multiprocess\$3	USPAT;	2004/03/01 08:58
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
1	4	allian as and huffman in	IBM_TDB	2004/02/04 09:50
-	17	silicon.as. and huffman.in.	USPAT; US-PGPUB;	2004/03/01 08:59
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
_	0	arbitration adj queue adj circuit	USPAT:	2004/03/01 09:13
		arbitation adj quodo adj orrodit	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
1			IBM_TDB	
-	0	((arbit?r arbitration) with (queue adj circuit)) same cache	USPAT;	2004/03/01 09:13
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	109	arhitration adjaugue	IBM_TDB USPAT;	2004/03/01 09:13
-	109	arbitration adj queue	US-PGPUB;	2004/03/01 09.13
			EPO; JPO;	
			DERWENT;	
			IBM TDB	
-	8	(arbitration adj queue) same cache	USPAT;	2004/03/01 09:15
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_	and the said of th	IBM_TDB	2004/02/04 00:47
-	0	collapsible adj3 queue	USPAT; US-PGPUB;	2004/03/01 09:17
	-		EPO; JPO;	-
			DERWENT;	
			IBM_TDB	
-	67891	collapsible	USPAT;	2004/03/01 09:18
	]		US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	collapsible with queue	USPAT;	2004/03/01 09:18
			US-PGPUB;	1
			EPO; JPO;	
			DERWENT;	İ
<u> </u>	866	distributed adj shared adj memory	USPAT;	2004/03/01 09:20
		alothodica adjuniared adjunemery	US-PGPUB;	2304/00/01 05.20
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	

				_
-	414	(distributed adj shared adj memory) same processor\$1	USPAT; US-PGPUB;	2004/03/01 09:21
			EPO; JPO;	
			DERWENT;	
	14	(distributed adj shared adj memory) same processor\$1 same	USPAT;	2004/03/04 00:24
-	14		US-PGPUB:	2004/03/01 09:31
		(memory adj interface)	EPO; JPO;	
			DERWENT;	
ŀ			IBM_TDB	
į	4	priority same (idle adj state) same win\$4 same busy	USPAT;	2004/03/01 09:33
-	*	priority same (idle adj state) same willipa same busy	US-PGPUB;	2004/03/01 09.33
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
	0	(hierarchical adj (mux multiplexer)) same priority	USPAT;	2004/03/01 09:38
-	"	(merarchical adj (max malapiexer)) same phonty	US-PGPUB;	2004/00/01 00:00
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	6	(hierarchical with (mux\$3 multiplexer\$3)) same priority	USPAT;	2004/03/01 09:38
		(meralement that (make maniplexeree)) dame priority	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
1_	330	(arbitration arbit?r) adj3 queue	USPAT;	2004/03/01 10:31
}	000	(arbitration arbitrit) adjo quodo	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	7545	collaps\$3 same (logic circuit mux\$3 multiplex\$3)	USPAT;	2004/03/01 10:56
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	collaps\$3 same (logic circuit mux\$3 multiplex\$3) same 2:1	USPAT;	2004/03/01 10:56
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	28	collaps\$3 same (logic circuit mux\$3 multiplex\$3) same queue	USPAT;	2004/03/01 10:56
	i		US-PGPUB;	
	[		EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/00/04 11 5=
-	129	(queue fifo) same (updat\$3 adj3 (logic circuit ))	USPAT;	2004/03/01 11:27
			US-PGPUB;	
†		·	EPO; JPO;	
			DERWENT;	
	00000	(average fife) name (refresh@2 (legis signifit))	IBM_TDB	2004/02/04 44:20
-	23262	(queue fifo) same (refresh\$3 (logic circuit ))	USPAT; US-PGPUB;	2004/03/01 11:28
			EPO; JPO;	
1			DERWENT;	
	1		IBM_TDB	]
1_	940	(queue fifo) same (refresh\$3 (logic circuit )) same idle	USPAT;	2004/03/01 11:28
-	340	(quous mo) same (remeshipo (logio elledit )) same idie	US-PGPUB;	
1			EPO; JPO;	1
			DERWENT;	
	1		IBM_TDB	
1-	223	(queue fifo) with (refresh\$3 (logic circuit )) with idle	USPAT;	2004/03/01 11:29
	223	(4222) may man ( 20123) 42 (128) 20122 ( ) min ( 2012)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
	<b></b>	I		·

-	3	(queue fifo) with (refresh\$3 adj (logic circuit )) with idle	USPAT; US-PGPUB;	2004/03/01 11:29
			EPO; JPO;	
			DERWENT;	
		(	IBM_TDB	0004/00/04 40 00
-	0	(queu\$3 adj3 operation) same enque same deque	USPAT;	2004/03/01 13:36
			US-PGPUB; EPO; JPO;	
			DERWENT;	
			IBM_TDB	
_	21	enque same deque	USPAT;	2004/03/01 13:51
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	559	(queue\$3 queu\$3) adj operation	USPAT;	2004/03/01 14:10
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
-	25	((queue\$3 queu\$3) adj operation) same (mark\$3 idle)	USPAT:	2004/03/01 14:00
_	25	((quodoto quodito) daj operation) same (mainte idio)	US-PGPUB;	230 1100/01 17:00
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	120	(mux\$3 mutiplex\$3) same (fifo\$1 queu\$4) same (updat\$3	USPAT;	2004/03/01 14:01
		refresh\$3)	US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
	584	(queue\$3 queu\$3) same (fragment\$5 defragment\$5	USPAT;	2004/03/01 14:10
-	304	de-fragment\$5)	US-PGPUB;	2004/05/01 14:10
	]	de-nagmentes)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	128	(queu\$4 adj3 manag\$4) same (mux\$3 multiplex\$3)	USPAT;	2004/03/01 14:18
			US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
	139	queue same shift same (mux\$3 multiplex\$3)	USPAT:	2004/03/01 14:45
-	100	quede same same (maxes maniplexes)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
	_		IBM_TDB	0004/00/24 40 22
-	73	(memory adj interface) same cache same (arbit?r (arbitration	USPAT;	2004/03/01 16:29
		adj (logic circuit)))	US-PGPUB; EPO; JPO;	
-			DERWENT;	
			IBM_TDB	
-	28	compaction same queue	USPAT;	2004/03/02 08:13
		·	US-PGPUB;	
-	}		EPO; JPO;	
			DERWENT;	
	_	initializes como idlo como (all adi2 antrica) como (aucus adi	IBM_TDB	2004/02/02 12:02
-	0	initializ\$5 same idle same (all adj3 entries) same (queue adj location\$1)	USPAT; US-PGPUB;	2004/03/02 12:02
			EPO; JPO;	
	<u> </u>		DERWENT;	
			IBM_TDB	
-	0	initializ\$5 same idle same (all adj3 entries) same queue	USPAT;	2004/03/02 12:02
	]		US-PGPUB;	
			EPO; JPO;	
			DERWENT; IBM_TDB	
L	l		T IDINI I DD	<u> </u>

22   Initializ\$\$ same idle same enir\$3 same queue   USPAT: USPA					
- 0 initializ\$5 same (null empty idle) same (all adj3 entries) same (queue adj location\$1)  - 0 initializ\$5 same (null empty idle) same (all adj3 entries) same queue  - 0 initializ\$5 same (null empty idle) same (all adj3 entries) same queue  - 178 initializ\$5 same (null empty idle) same entr\$3 same queue  - 18	•	22	initializ\$5 same idle same entr\$3 same queue		2004/03/02 12:02
- 0 initializ\$5 same (null empty idle) same (all adj3 entries) same (group adj location\$1) 2004/03/02 12:02 USPAT; USPGPUB; EPO; JPO; DERWENT; IBM_TDB USP					
178					
0   initializ\$5 same (null empty idle) same (all adj3 entries) same   USPĀT;   USPGPUB;   EPO; JPO;   DERWENT;   IBM_TDB   USPĀT;   USPGPUB;   EPO; JPO; DERW				·	
Queue adj location\$1   US-PGPUB; EPO; JPO; DERWENT; IBM, TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM, TDB USP		_			
Proj. proj. programme	-	0			2004/03/02 12:02
DERWENT   BM_TDB   USPAT;			(queue adj location\$1)		
IBM TDB USPAT; US-PCPUB; EPC; JPC; DERWENT; IBM TDB USPAT; US-PC					
178					
178					
178	-	0			2004/03/02 12:02
178			queue		
178				•	
178					
US-PGPUB: EPO: JPO: DERWENT: IBM TDB USPAT: US-PGPUB: EPO: JPO:			n		
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM	-	178	initializ\$5 same (null empty idle) same entr\$3 same queue		2004/03/02 12:05
DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USP					
4					
4					
September   Sept					
- 29 (initializ\$5 with queue) same ((null empty idle) with entr\$3)  - 10 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 10 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 10 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3)  - 158 distributed adj shared adj memory adj system  - 157 CCNUMA  - 158 (distributed adj shared adj memory adj system)  - 159 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 150 (memory adj interface) same ((plurality plural) adj3 (processor\$1 inicroprocessor\$1 cpu\$1)) same (shar\$3 adj (memory.)  - 158 (processor\$1 inicroprocessor\$1 cpu\$1)) same (shar\$3 adj (memory.)  - 159 (memory ram sram dram))	-	4	(initializ\$5 adj3 queue) same ((null empty idle) with entr\$3)	•	2004/03/02 12:43
- 29 (initializ\$5 with queue) same ((null empty idle) with entr\$3)  - 10 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 10 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 15 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with entr\$3)  - 158 distributed adj shared adj memory adj system  - 157 CCNUMA  - 157 CCNUMA  - 158 (distributed adj shared adj memory adj system)  - 159 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 157 CCNUMA  - 158 (distributed adj shared adj memory adj system)  - 159 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 157 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory adj system)  - 158 (memory ram sram dram))					
BM TDB					
- 10 (initializ\$5 with queue) same ((null empty idle) with entr\$3) USPAT; US-PGPUB; PPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; PPO; JPO; DERWENT; PPO; JP					
- 10 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 0 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 0 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with entr\$3)  - 157 CCNUMA  - 157 CCNUMA  - 157 CCNUMA  - 157 CCNUMA  - 157 (distributed adj shared adj memory adj system)  - 157 (distributed adj shared adj memory adj system)  - 157 (distributed adj shared adj memory adj system)  - 157 (distributed adj shared adj memory adj system)  - 157 (distributed adj shared adj memory adj system)  - 157 (distributed adj shared adj memory adj system)  - 158 (distributed adj shared adj memory adj system)  - 159 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 158 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 158 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 158 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 158 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 159 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 159 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 159 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 150 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 150 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj sys					0004/00/00 10 15
- 10 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 0 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3)  - 157 CCNUMA  - 12 (memory adj interface) same ((plurallity plural) adj3 (processor\$1 microprocessor\$1 cpu\$1) same (shar\$3 adj (memory ram sram dram))  - 12 (memory ram sram dram)  - 150 initializ\$5 with (queue with entr\$3) with all  - 157 CCNUMA  - 157 CCNUMA  - 158 (distributed adj shared adj memory adj system)  - 159 (memory adj interface) same ((plurallity plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))	-	29	(initializ\$5 with queue) same ((null empty idle) with entr\$3)		2004/03/02 12:43
- 10 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 0 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with en					
- 10 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 0 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3)  - 158 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with entr\$3)  - 158 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with en					
- 10 initializ\$5 with (queue with entr\$3) with (null idle empty)  - 0 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with entr\$3)  - 158 distributed adj shared adj memory adj system  - 157 CCNUMA  - 157 CCNUMA  - 158 (distributed adj shared adj memory adj system)  - 159 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system)  - 150 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 150 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  - 150 (memory ram sram dram))					
US-PGPUB; EPC; JPC; DERWENT; IBM_TDB USPAT; US-PGPUB; EPC; JPC; DERWENT; IBM_TDB US-PGPUB; EPC; DERWENT;					0004/00/00 40 04
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; I	-	10	initializ\$5 with (queue with entr\$3) with (null idle empty)		2004/03/02 13:01
- 0 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3) - 157 initializ\$5 with (queue with entr\$3) - 183 distributed adj shared adj memory adj system - 183 cCNUMA - 57 CCNUMA - 185 (distributed adj shared adj memory adj system) - 186 (distributed adj shared adj memory adj system) - 187 (distributed adj shared adj memory adj system) - 188 (distributed adj shared adj memory adj system) - 189 (distributed adj shared adj memory adj system) - 190 (distributed adj shared a					
- 157 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with entr\$3)  - 183 distributed adj shared adj memory adj system  - 183 distributed adj shared adj memory adj system  - 57 CCNUMA  - 57 CCNUMA  - 157 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory)  - 12 (memory ram sram dram))  - 157 initializ\$5 with (queue with entr\$3) with all  USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB					
- 157 initializ\$5 with (queue with entr\$3) with all  - 157 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with entr\$3)  - 183 distributed adj shared adj memory adj system  - 183 distributed adj shared adj memory adj system  - 57 CCNUMA  - 57 CCNUMA  - 157 (distributed adj shared adj memory adj system)  - 158 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 159 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory interface) same (shar\$3 adj (memory ram sram dram))  - 157 (memory adj interface) same (shar\$3 adj (memory interface) same (shar\$3 adj					
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISM_TDB USPAT; ISM_TDB			Light Committee and the second of the second		2004/02/02 42:04
- 157 initializ\$5 with (queue with entr\$3)  - 157 initializ\$5 with (queue with entr\$3)  - 183 distributed adj shared adj memory adj system  - 183 distributed adj shared adj memory adj system  - 57 CCNUMA  - 57 CCNUMA  - 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory; adj system); EPO; JPO; DERWENT; E	-	0	initializ\$5 with (queue with entr\$3) with all		2004/03/02 13:01
- 157 initializ\$5 with (queue with entr\$3)  - 183 distributed adj shared adj memory adj system  - 183 distributed adj shared adj memory adj system  - 57 CCNUMA  - 57 CCNUMA  - 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory. Interface) per popular plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory. Interface) per popular plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (processor\$1 cpu\$1)					
- 157 initializ\$5 with (queue with entr\$3)  - 183 distributed adj shared adj memory adj system  - 183 distributed adj shared adj memory adj system  - 57 CCNUMA  - 57 CCNUMA  - 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory; place)   12 (memory ram sram dram))  - 157   IBM_TDB   12 (12 (13 (14 (14 (14 (14 (14 (14 (14 (14 (14 (14					
- 157 initializ\$5 with (queue with entr\$3)  - 183 distributed adj shared adj memory adj system  - 183 distributed adj shared adj memory adj system  - 57 CCNUMA  - 57 CCNUMA  - 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory; adj system) and (processor\$1 cpu\$1)) same (shar\$3 adj (pro					
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DE		457	initialines with (many with antes)		2004/02/02 12:01
- 183 distributed adj shared adj memory adj system  - 187 CCNUMA  - 57 CCNUMA  - 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ; JPO; DERWENT; IBM_TDB (JS-PGPUB; EPO; JPO; DERWENT; ISM_TDB (JS-PGPUB; EPO;	-	15/	initializas with (queue with entras)		2004/03/02 13.01
- 183 distributed adj shared adj memory adj system  - 57 CCNUMA  - 57 CCNUMA  - 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; D					
- 183 distributed adj shared adj memory adj system  - 57 CCNUMA  - 57 CCNUMA  - 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory; adj system) adj  - 12 (memory ram sram dram))  - 18					
- 183 distributed adj shared adj memory adj system  - 57 CCNUMA  - 57 CCNUMA  - 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory; adj system);   USPAT; US-PGPUB; EPO; JPO; DERWENT;   IBM_TDB   USPAT; US-PGPUB; EPO; JPO; DERWENT;   IBM_TDB   USPAT;   US-PGPUB;   EPO; JPO; DERWENT;   IBM_TDB   USPAT;   US-PGPUB;   EPO; JPO; DERWENT;   IBM_TDB   USPAT;   US-PGPUB;   EPO; JPO; DERWENT;   IBM_TDB   USPAT;   US-PGPUB;   US-PGPUB;   IBM_TDB   USPAT;   US-PGPUB;   IBM_TDB   USPAT;   US-PGPUB;   US-PGPUB;   US-PGPUB;   US-PGPUB;   US-PGPUB;   US-PGPU					
- 57 CCNUMA  57 CCNUMA  58 CCNUMA  59 CCNUMA  50 CONUMA	1	192	distributed adi shared adi memory adi syetem		2004/03/02 18:16
EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; ISPO; JPO; DERW	-	'03	aistributou auj sirareu auj memory auj system		2007/00/02 10:10
- 57 CCNUMA  CONUMA  C					
- 57 CCNUMA    IBM_TDB	1 -	<u> </u> -	· · · · · · · · · · · · · · · · · · ·		
- S7 CCNUMA  USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB  (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB  USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;					
- 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;	-	57	CCNUMA		2004/03/02 18:18
- 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;		"			-3555.52 .5.16
- 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  DERWENT; IBM_TDB US-PGPUB; EPO; JPO; DERWENT; IBM_TDB US-PAT; US-PGPUB; EPO; JPO; DERWENT;					
- 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))    IBM_TDB   US-PGPUB; EPO; JPO; DERWENT; IBM_TDB   US-PAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB					
- 35 (distributed adj shared adj memory adj system) and @ad<20000720 and (arbit?r arbitration)  - 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  USPAT; US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;					
@ad<20000720 and (arbit?r arbitration)  US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  US-PGPUB; EPO; JPO; DERWENT; US-PGPUB; EPO; JPO; DERWENT;	.	35	(distributed adi shared adi memory adi system) and		2004/03/02 18:21
- 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;					
- 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;					
- 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;					
- 12 (memory adj interface) same ((plurality plural) adj3 (processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  USPAT; US-PGPUB; EPO; JPO; DERWENT;					
(processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj (memory ram sram dram))  US-PGPUB; EPO; JPO; DERWENT;	-	12	(memory adj interface) same ((plurality plural) adj3		2004/03/03 09:00
(memory ram sram dram)) EPO; JPO; DERWENT;		_			
DERWENT;					
IBM_TDB			. "		
	L	<u> </u>			

-	1	(memory adj interface) same ((plurality plural) adj3	USPAT;	2004/03/03 09:00
		(processor\$1 microprocessor\$1 cpu\$1)) same (shar\$3 adj	US-PGPUB;	
		(memory ram sram dram)) same (arbit?r arbitration)	EPO; JPO;	
		,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,, ,,	DERWENT:	
			IBM TDB	
_	21	arbit?r adj3 (cache adj memory)	USPAT;	2004/03/03 11:23
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	316	(memory adj interface) with (cache adj memory)	USPAT;	2004/03/03 11:23
			US-PGPUB;	
			EPO; JPO;	:
			DERWENT;	
			IBM_TDB	
-	0	(memory adj interface) with (cache adj memory) with	USPAT;	2004/03/03 11:24
		((arbitration arbit?r) adj3 queue)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	0	(memory adj interface) with (cache adj memory) with	USPAT;	2004/03/03 11:24
		((arbitration arbit?r) with queue)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	1	(memory adj interface) same cache same ((arbitration arbit?r)	USPAT;	2004/03/03 11:51
		with queue)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM TDB	